Course Content
Transmission

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Name of the course:	Digital Multiplexing
Duration:	5 days
Venue:	Welisara

Course content:

- De briefing of P-MUX and E1 frame information
- Digital hierarchies
- Block diagram of higher order multiplexers
- Bit interleaving multiplexing and De-multiplexing
- Principles of higher order multiplexing
- Principles of justification
- 8M, 34M, 140M frame structure
- Fixed frequency and justification jitter
- Stuffing ratio
- Phase variation of write clock and read clock
- Frame alignment loss and recovery
- Frame alignment detection and BER measurement
- Practical on PDH MUX equipments
    - ❖ Alarms
    - ❖ Loop Back
    - ❖ Frame Alignment Loss and Recovery
    - ❖ BER measurements
    - ❖ JITTER identification
    - ❖ JITTER tolerance test